

FIG. 1

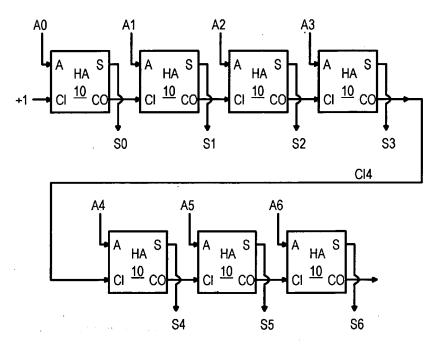


FIG. 2

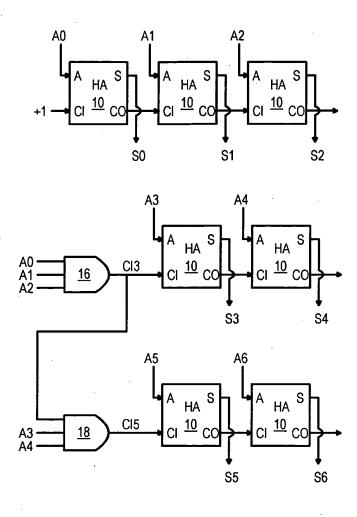


FIG. 3

CURRENT STATE A		NEXT STATE S
0000		0001
0001		0010
0010		0011
0011		0100
0100		0101
0101	LOW DITC	0110
0110	LOW BITS = 111	0111
0111 _	$\ddot{\gamma}$ .	1000
1000	XOR WITH	1001
1001	HIGH BIT	1010
1010		1011
1011		1100
1100		1101
1101	LOWE	1110
1110	LOW BITS = 111	1111
1111 _	$\tilde{\gamma}$ ,	0000
	XOR WITH HIGH BIT	

FIG. 4

